

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Prior Application: Y. KANNO et al
Serial No. 10/325,920
Filed: December 23, 2002

Group Art: 2818
Examiner: G. Auduong
For: SEMICONDUCTOR INTEGRATED CIRCUIT

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

June 27, 2003

Sir:

Prior to examination, please amend the above application
as follows.